THE COMMISSIONER OF FATENTS AND TRADEMARKS SHINGTON, D.C. 20231

Sir:

Transmitted herewith for filing is the Utility patent application of:

INVENTOR: Hirokatsu HAYASHI and Toshiro TAKAHASHI

FOR:

LEVEL SHIFT CIRCUIT AND SEMICONDUCTOR

INTEGRATED CIRCUIT

Enclosed are:

- □ 10 sheets of □ formal □ informal drawings.
- Notice to claim priority date of October 30, 2000.
- ☑ A preliminary amendment.
- ☐ Applicant requests non-publication.
- ☑ An Information Disclosure Statement with PTO Form 1449.
- Machine An assignment of the invention to Hitachi, Ltd. and Hitachi ULSI Systems Co., Ltd.
- The filing fee is calculated as shown below:

For	Number Filed		Number Extra	RATE	CALCULATIONS
TOTAL CLAIMS	22	-20	. 2	x\$18 =	36.00
INDEPENDENT CLAIMS	5	-3	2	x\$84 =	168.00
MULTIPLE DEPENDE	\$280.00				
			BASIC FEE		\$740.00
		eta ya	TOTAL OF ABOVE		1,224.00
REDUCTION BY 1/2 FOR FI	LING BY SMA	L ENTITY (no:	te 37 C.F.R. §§ 1.9, 1.27, 1.2	28).	
				TOTAL	1,224.00

	ge my Deposit Account Number _ plicate copy of this paper is enclose		in the amount of	to cover the filing and assignment recordation
--	---	--	------------------	--

- A check in the amount of \$1,264.00 to cover the filing fee and assignment recordation fee is enclosed.
- The Commissioner is hereby authorized to charge any additional fees associated with this communication, including patent application filing fees and processing fees under 37 C.F.R. § 1.16 and 1.17, or credit any overpayment to Deposit Account Number 08-1480.

Respectfully submitted

Stanley P. Fisher

Registration Number 24,344

JUAN CARLOS A. MARQUEZ Registration No. 34,072

REED SMITH HAZEL & THOMAS LLP 3110 Fairview Park Drive Suite 1400 Falls Church, Virginia 22042 (703) 641-4200 October 15, 2001